These are the changes to the 1.091 revision made since the 1.086 revision.

Section 1.1

A new paragraph was added requiring that the external view of state machines be equivalent to the spec, even if the implementation differs.

Section 1.2

A definition of frame was made to include interlaced video formats.

Section 2.2

The two second Ri verification rate is clarified as being a minimum—more frequent verification is permissible.

The V computation byte order was clarified. See also the new test vectors in Appendix A.

An enhanced Ri computation is available as an option to prevent permanent “snow” when the transmitter and receiver lose synchronization.

An additional proposal to assist in maintaining synchronization is noted at the end of the section. This has not yet received appropriate review, but is included for consideration.

Section 2.3

An attempt to clarify when a receiver is ready to authenticate was added.

Section 2.4

A proposal to permit more rapid re-authentication was added to the end of the section. This has not yet received appropriate review, but is included for consideration.

Section 2.5

Corrected an error in the F10_F0 transition: “no active HDCP Receiver” to “an active HDCP Receiver”.

Section 2.6

The Ainfo port has a new bit to support enhanced Ri computation.

The V port has been broken down to five integers to clarify the communication of the SHA-1 digest computation.

The Bcaps HDMI_CAPABLE bit has been renamed to HDMI_RESERVED, since it is now obsolete, and the enhanced Ri computation capability has been added.

The detection of an HDMI-capable receiver is now specified in the HDMI document.

Corrected the Irate description to match section 2.2.

A proposal for the resolution of ENC_EN or ENC_DIS ambiguity is included. There are several new features and proposals addressing this issue.

The keep-out period is defined more clearly.

Appendix A

Tables 24 and 25 provide two test vectors for the repeater V computation.

Appendix C

A sample algorithm for performing the Ri match check is provided. This algorithm also aids in detecting loss of frame encryption synchronization. The enhanced Ri computation variant is also presented.